



P 040

Location date

9/19/01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Parruck et al.

Assignee: Azanda Network Devices

Title: "MULTI-SERVICE SEGMENTATION AND REASSEMBLY
DEVICE THAT MAINTAINS ONLY ONE REASSEMBLY
CONTEXT PER ACTIVE OUTPUT PORT"

Serial No.: 09/851,565

Filed: May 8, 2001

Examiner:

Tel:

Docket No.: AZA-003/2001-P003

Art Unit:

Box DD

Assistant Commissioner For Patents
Washington, D.C. 20231

September 12, 2001

Sir:

INFORMATION DISCLOSURE STATEMENT

Pursuant to 37 C.F.R. §1.56, §1.97 and §1.98, Applicants bring the 48 documents listed on the enclosed form PTO-1449 to the Examiner's attention in the above-captioned application. Citation of the listed documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant application;
2. a representation that a search has been made; or
3. an admission that the information cited is, or is considered to be, material to patentability as defined in §1.56(b).

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Box DD, Assistant Commissioner for Patents, Washington, D.C. 20231, on September 12, 2001

September 12, 2001
Date

Lester Wallace
T. Lester Wallace

Respectfully submitted,

T. Lester Wallace
Attorney for Applicants
Reg. No. 34,748

U.S. Department of Commerce, Patent & Trademark Office O P I E SEP 19 2001 C 50		Application No.: 09/851,565
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)		Filing date: May 8, 2001 Inventors: Parruck, et al. Group Art Unit: unknown
"Multi-Service Segmentation and Reassembly Device That Maintains Only One Reassembly Context Per Active Output Port"		Examiner name: unknown
		Atty. Docket: AZA-003/2001-P003

U.S. Patent Documents

*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
AA	5,666,353	09/09/97	Klausmeier et al.	370	230	03/21/95
AB	5,742,765	04/21/98	Wong et al.	395	200	06/19/96
AC	5,787,255	07/28/98	Parlan et al.	395	200	04/12/96
AD	5,802,052	09/01/98	Venkataraman	370	395	01/26/96
AE	5,802,287	09/01/98	Rostoker et al.	395	200	08/03/95
AF	5,809,021	09/15/98	Diaz et al.	370	364	06/10/96
AG	5,838,915	11/17/98	Klausmeier et al.	395	200	11/17/97
AH	5,859,835	01/12/99	Varma et al.	370	229	04/15/96
AI	5,867,480	02/02/99	Thomas et al.	370	230	09/12/96
AJ	5,870,394	02/09/99	Oprea	370	392	07/23/96
AK	5,898,688	04/27/99	Norton et al.	370	362	05/24/96
AL	5,905,725	05/18/99	Sindhu et al.	370	389	04/18/97
AM	5,917,820	06/29/99	Rekhter	370	392	06/10/96
AN	5,917,828	06/29/99	Thompson	370	474	01/03/97
AO	5,920,705	07/06/99	Lyon et al.	395	200	01/30/97
AP	5,946,313	08/31/99	Allan et al.	370	397	03/20/97
AQ	5,963,541	10/05/99	Koga et al.	370	229	11/09/95
AR	5,987,034	11/16/99	Simon et al.	370	465	08/30/96
AS	6,026,086	02/15/00	Lancelot et al.	370	353	01/08/97
AT	6,034,945	03/07/00	Hughes et al.	370	230	05/15/96
AU	6,052,379	04/18/00	Iverson et al.	370	442	09/18/98
AV	6,088,355	07/11/00	Mills et al.	370	392	10/11/96
AW	6,091,725	07/18/00	Cheriton et al.	370	392	12/29/95
AX	6,101,188	08/08/00	Sekine et al.	370	401	09/10/97
AY	6,111,877	08/29/00	Wilford et al.	370	392	12/31/97

Examiner

Date Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

U.S. Department of Commerce Patent & Trademark Office				Application No.: 09/851,565			
SEP 19 2001				Filing date: May 8, 2001			
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)				Inventors: Parruck, et al.			
"Multi-Service Segmentation and Reassembly Device That Maintains Only One Reassembly Context Per Active Output Port"				Group Art Unit: unknown			
				Examiner name: unknown			
				Atty. Docket: AZA-003/2001-P003			
U.S. Patent Documents							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	BA	6,134,217	10/17/00	Stiliadis et al.	370	232	04/16/96
	BB	6,144,662	11/07/00	Colmant et al.	370	390	07/09/96
	BC	6,147,999	11/14/00	Honda et al.	370	396	05/27/99
	BD	6,157,641	12/05/00	Wilford	370	389	08/22/97
	BE	6,160,793	12/12/00	Ghani et al.	370	236	10/13/98
	BF	6,167,027	12/26/00	Aubert et al.	370	230	09/23/97
	BG	6,169,739	01/02/01	Isoyama	370	395	01/08/98
	BH	6,175,570	01/16/01	Cukier et al.	370	414	12/04/96
	BI	6,185,212	02/06/01	Sakamoto et al.	370	395	04/08/98
	BJ	6,188,671	02/13/01	Chase et al.	370	232	12/10/97
Examiner _____ Date Considered _____							
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OCT 1 8 2001 PATENT & TRADEMARK OFFICE US		Atty. Docket: AZA-003/2001-P003						
Foreign Patent Documents								
		Document Number	Date	Country	Class	Subclass	Yes	No
	CA	WO 97/43869	11/20/97	international	H04Q			X
	CB	WO 99/17182	04/08/99	international	G06F			X
	CC	WO 00/56012	09/21/00	international	H04L			X
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
	CD	Application 09/528,802, filed 03/20/00, "Circuits for Combining ATM and Packet Data on an Optical Fiber and Methods Therefor," by Parruck, Nguyen, Ramakrishnan						
	CE	Application 09/539,479, filed 03/30/00, "Devices for Aggregating ATM Cells and Packets for Transmission Over the Same Channel," by Parruck, Nguyen, Ramakrishnan						
	CF	Application 09/539,306, filed 03/30/00, "Devices for Employing SONET Frames for Transporting both ATM Cells and Packets Over in the Same Channel in an Optical Fiber," by Parruck, Nguyen, Ramakrishnan						
	CG	Application 09/539,478, filed 03/30/00, "Methods and Apparatus for Dynamically Allocating Bandwidth Between ATM Cells and Packets," by Parruck, Nguyen, Ramakrishnan						
	CH	Application 09/539,461, filed 03/30/00, "Routers for Switching ATM Cells in a Packet-like manner Using a Packet Switch," by Parruck, Nguyen, Ramakrishnan						
	CI	Application 09/539,476, filed 03/30/00, "Processor-based Architecture for Facilitating Integrated Data Transfer between both ATM and Packet Traffic with a Packet Bus or Packet Link, Including Bidirectional ATM-to-Packet Functionality for ATM Traffic," by Parruck, Nguyen, Ramakrishnan						
	CJ	Application 09/539,477, filed 03/30/00, "Integrated ATM/Packet Segmentation-and-Reassembly Engine for Handling both Packet and ATM Input Data and for Outputting both ATM and Packet Data," by Parruck, Nguyen, Ramakrishnan						
	CK	"Is the Bloom Off Jasmine" and other pages from 'Light READING' web site "www.lightreading.com", 2 pages (printed 03/26/01).						
	CL	Chapter 3, Frame Relay Architecture, from "Frame Relay Technology and Practice" textbook, by Jeff T. Buckwalter, (copyright 2000).						
	CM	Chapter 5, MPLS Core Protocols, from "MPLS Technology and Applications" textbook, by Bruce Davie and Yakov Rekhter, (copyright 2000).						
Examiner		Date Considered						
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